

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Patent Application**

5 Applicant(s) Luk et al.  
Docket No.: YOR920030603US1  
Serial No.: 10/751,714  
Filing Date: January 5, 2004  
Group: 3663  
10 Examiner: Johannes P. Mondt

Title: Amplifiers Using Gated Diodes

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RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment  
Commissioner for Patents  
20 P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

25 This paper is responsive to the Examiner's Office Action dated January 25,  
2007.

The Examiner found that the claims of the instant application, which are  
directed to circuits and methods for amplifying signals, describe two distinct inventions. The  
30 Examiner therefore required that the application be restricted to one of the following  
inventions under 35 U.S.C. 121:

I. Claims 1-2 and 9-16, drawn to a circuit, classified in  
class 257, subclass 288+; and  
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II. Claims 21-23 and 29-35, drawn to a method for  
amplifying signals, classified in class 327, subclass 560+.

Applicants acknowledge with deep gratitude the Examiner's indication that  
40 Claims 3-8, 17-20, 24-28, 36 and 37 remain allowed.

Applicants respectfully assert that the restriction requirement is improper and  
should be withdrawn, since each Group is generally related to amplifying signals, and it is  
believed that a complete search for each Group would require a search of most, if not all, of  
the individual classes and subclasses. Accordingly, Applicants submit that an examination

